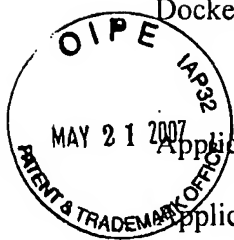


Docket No.: 071971-0015

PATENT



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Shiro SAKIYAMA, et al.

**RESPONSE UNDER 37 CFR 1.116  
EXPEDITED PROCEDURE**

Application No.: 10/511,165

Customer No.: 53080

Filed: October 14, 2004

Confirmation No.: 6689

Group Art Unit: 2816

Examiner: HILTUNEN, THOMAS J

Title: SEMICONDUCTOR INTEGRATED CIRCUIT

**AMENDMENT UNDER 37 CFR 1.116**

Mail Stop AF  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

*To be entered*  
*TJH*  
*05/25/07*  
In response to the Final Office Action dated January 19, 2007, having a three-month shortened statutory period for reply set to expire on April 19, 2007, a petition for a one month extension of time up to and including May 19, 2007 being filed concurrently herewith, please amend the above identified application as follows.

Amendments to the claims begin on page 2.

Remarks begin on page 7.